

PATENT NUMBER

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/996788	F	438	700	1765	chen, K

Naokatsu Ikegami

Method for manufacturing a semiconductor device

TITLE	APPLICANTS
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Method for manufacturing a semiconductor device

ISSUING CLASSIFICATION								
TITLE		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

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**TERMINAL
DISCLAIMER**

CLAIMS ALLOWED

Sheets Drwg:

Flgs. Drwg.

Print Fig.

Total Claims:

Print Claim for O.G.

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____

☐ The terminal ____ months of this patent have been disclaimed.

(Assistant Examiner)

(Date)

(Primary Examiner)

(Date)

(Legal Instruments Examiner)

(Date)

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